Amendments to the Specification:

Please replace paragraph [0027] with the following amended paragraph:

[0027] The processing platform 10 may comprise a power management system comprising one or more processes hosted on the CPU 12 and system memory 14 to communicate with one or more subsystems of the processing platform 10. For example, the power management system may place the subsystems in a reduced power state by providing sleep messages to the subsystems in response to detecting one or more events or conditions. The power management system may cause the subsystems to subsequently resume to a full power state in response to other events. For example, the power management system may place the processing platform 10 in a reduced power state in response to detecting a user input from a mechanical interface (not shown) or detecting an absence of activity at the CPU 12 (e.g., absence of interrupt signals from I/O devices). From the reduced power state, the power management system may transition one or more subsystems of the processing platform 10 to a full power state in response to, for example, a user input from the mechanical interface or detection of an interrupt to the CPU 12. The power management system may be formed according to the Advanced Configuration and Power Interface (ACPI) as illustrated in the ACPI Specification, Rev. 1.0b, Feb. 2, 1999 (hereinafter the "ACPI Specification"). However, these are merely examples of a power management system used in conjunction with a processing platform and embodiments of the present invention are not limited in these respects.